

FIGURE 1

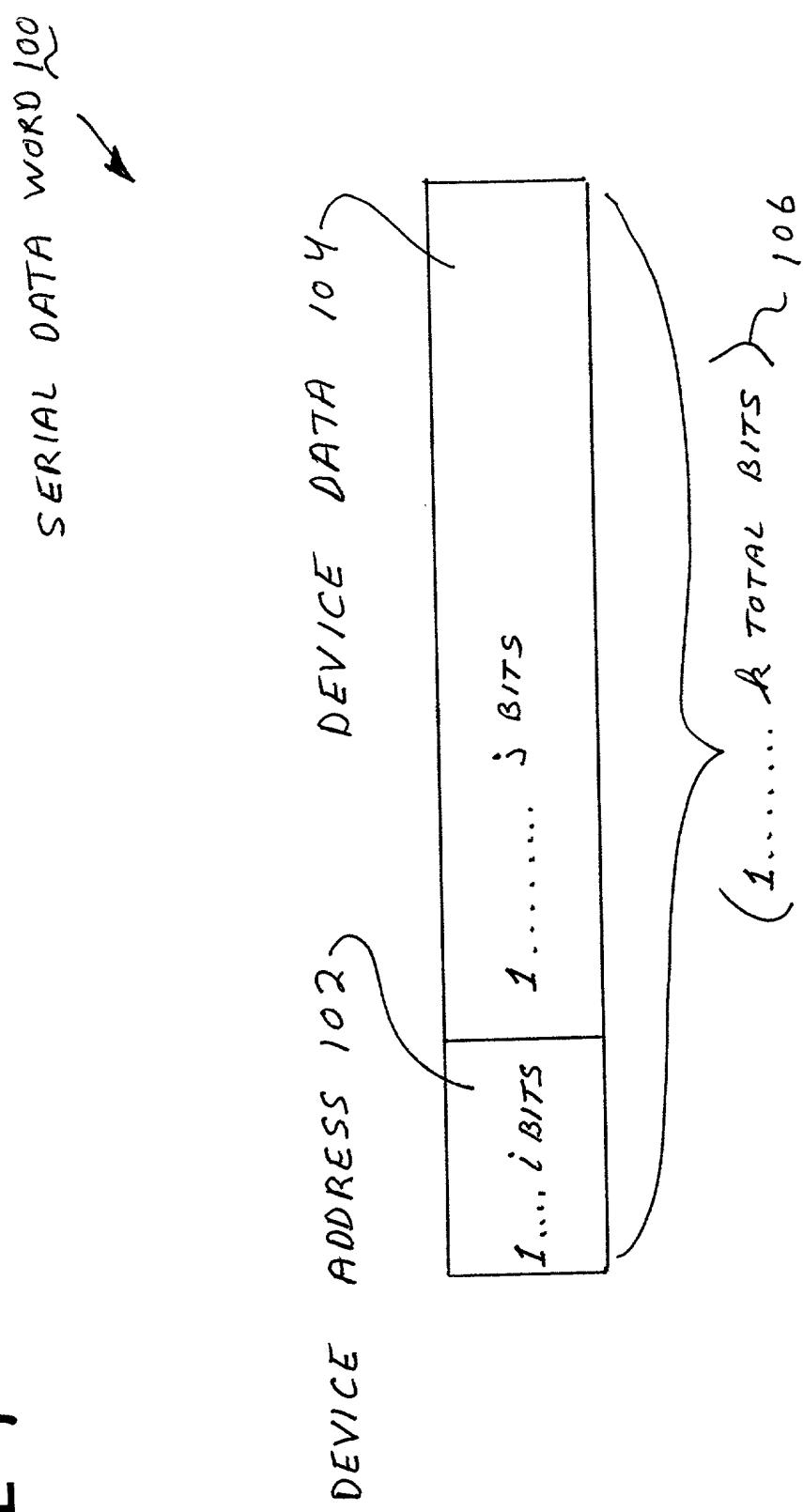


FIGURE 2

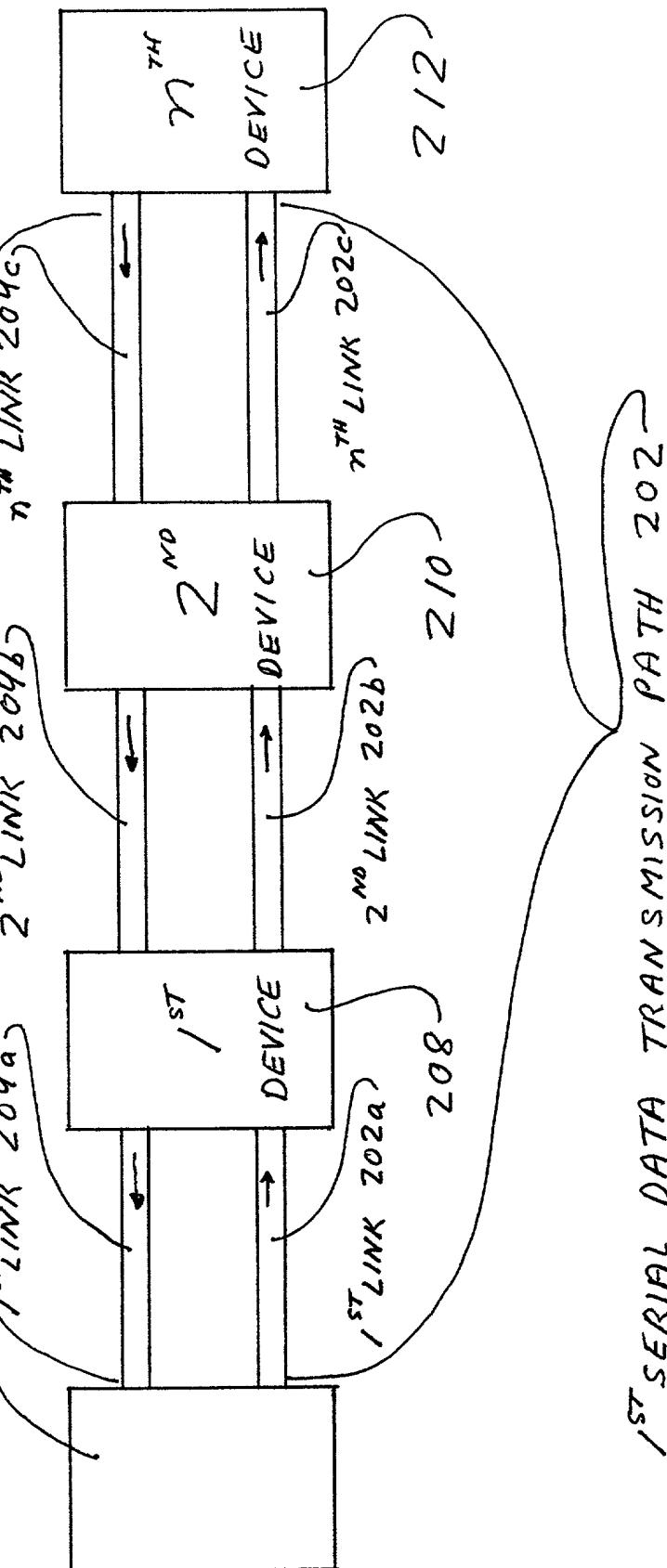
200

2nd SERIAL DATA TRANSMISSION PATH 204

CONTROLLER 206

1stLINK 204a

2ndLINK 204b



1st SERIAL DATA TRANSMISSION PATH 202

212

210

208

211

DEVICE

2nd

DEVICE

1st

DEVICE

FIGURE 3

